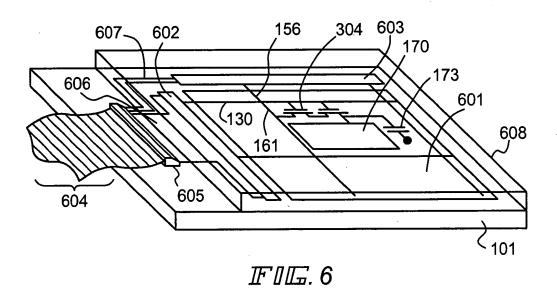
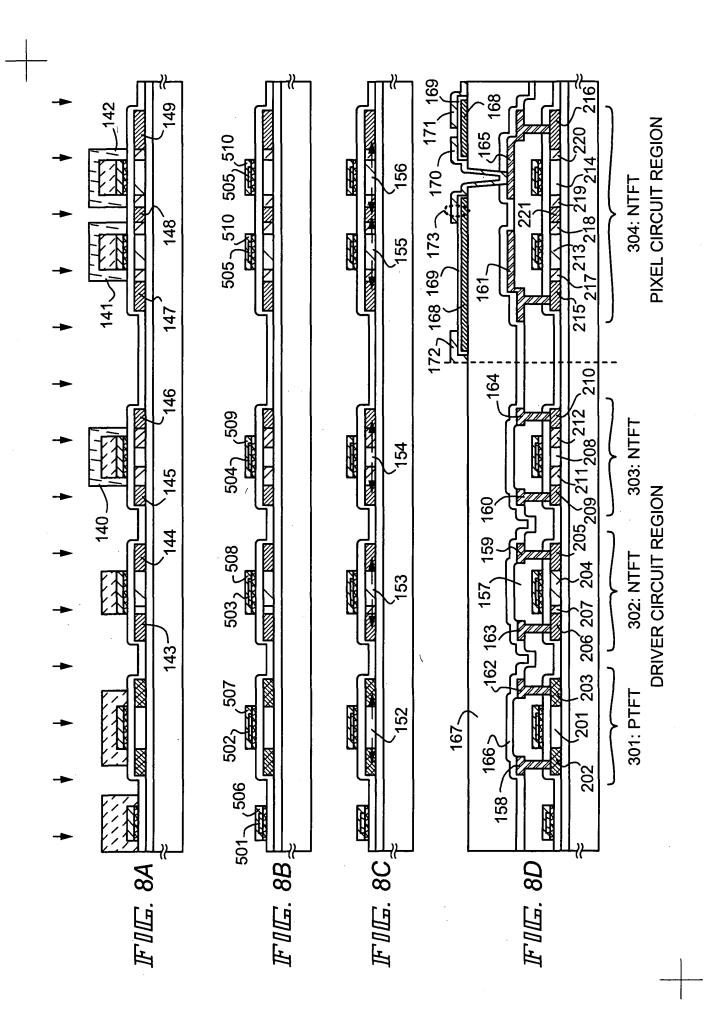


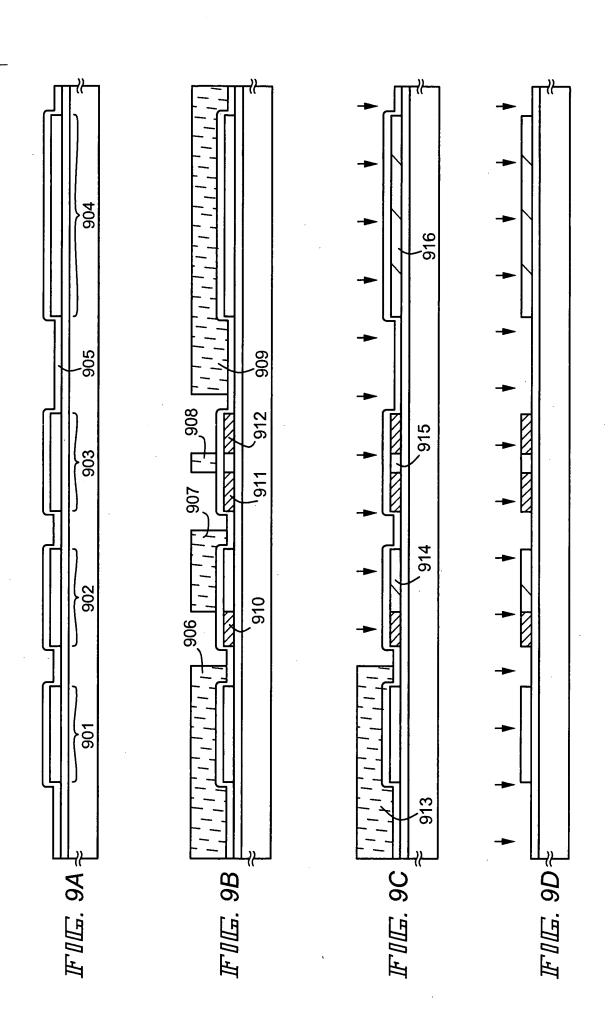
FI匠. 5

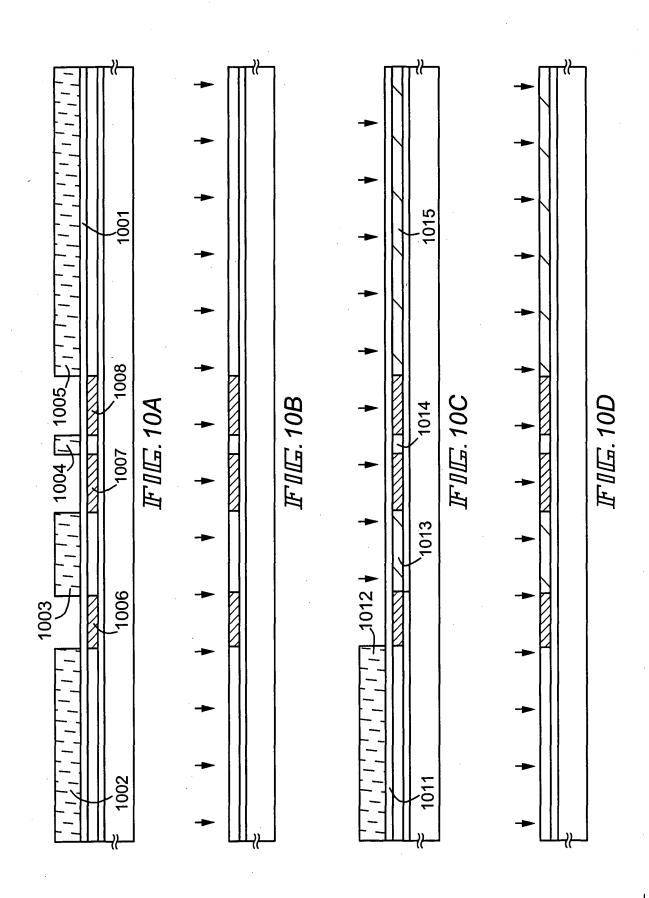


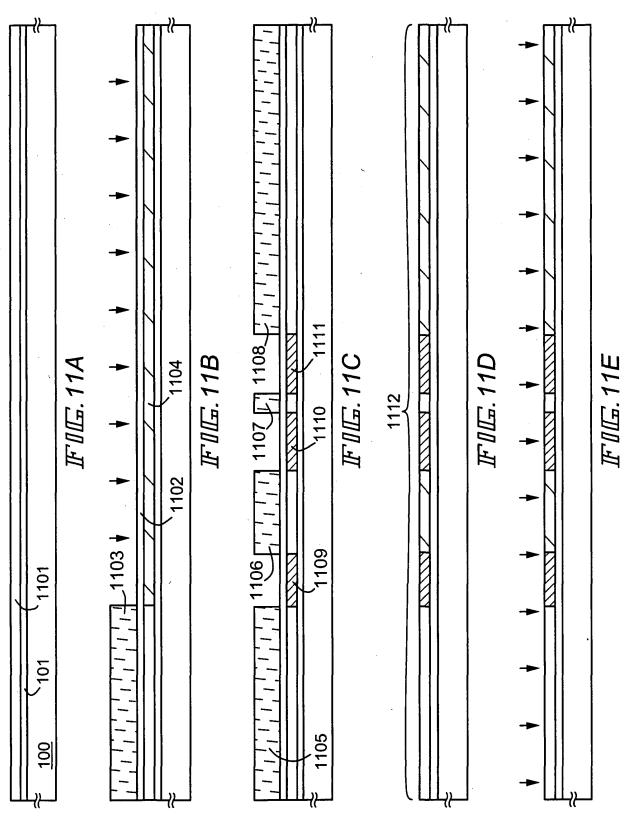
				701: SOURCE SIGNAL LINE SIDE DRIVER CIRCUIT	
				702: SHIFT REGISTER CIRCUIT	
				703: LEVEL SHIFTER CIRCUIT	
				704: BUFFER CIRCUIT	
708 709 710				705: SAMPLING CIRCUIT	·
GATE SIGNAL LINE SIDE & DRIVER CIRCUIT (A)	SHIFT REGISTER CIRCUIT	LEVEL SHIFTER CIRCUIT	BUFFER CIRCUIT	706: PIXEL CIRCUIT	GATE SIGNAL LINE SIDE 4 DRIVER CIRCUIT (B) :1
				712: PRE-CHARGE CIRCUIT	
			,		_

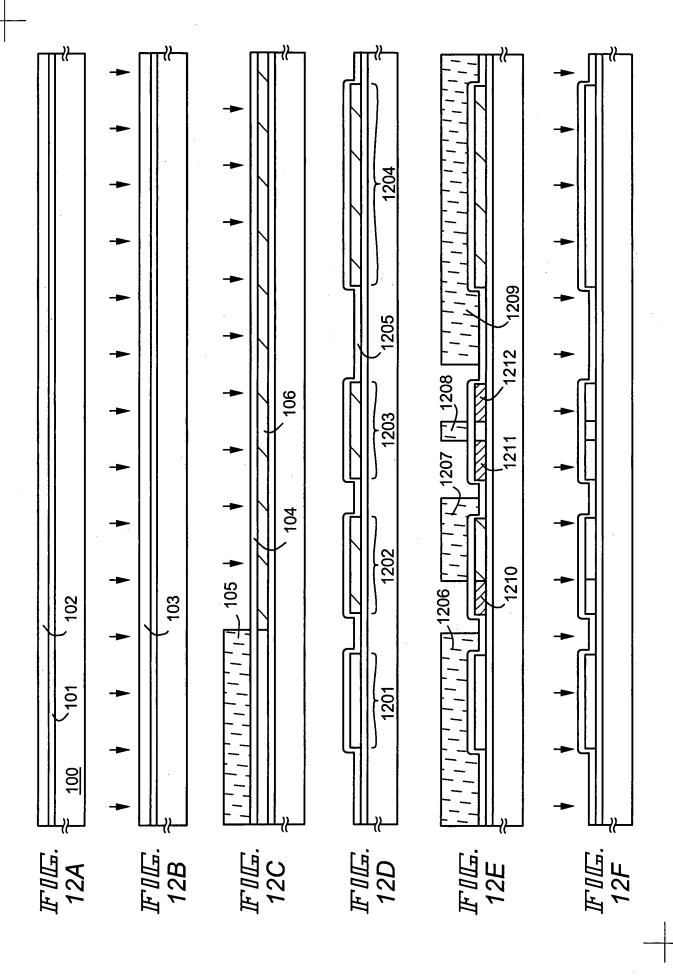
FII匠. 7

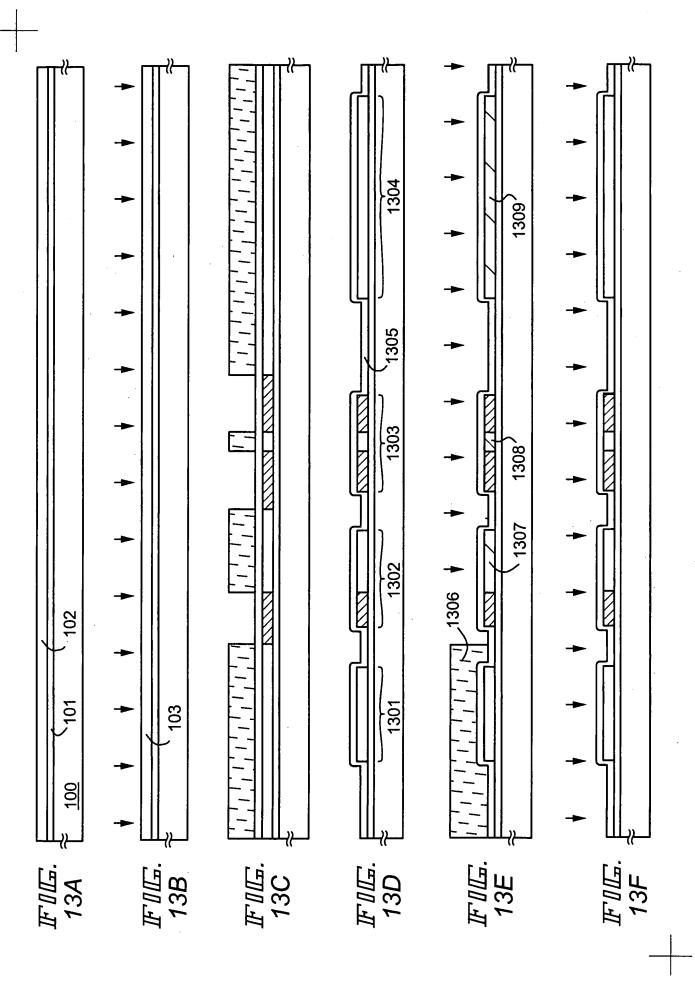


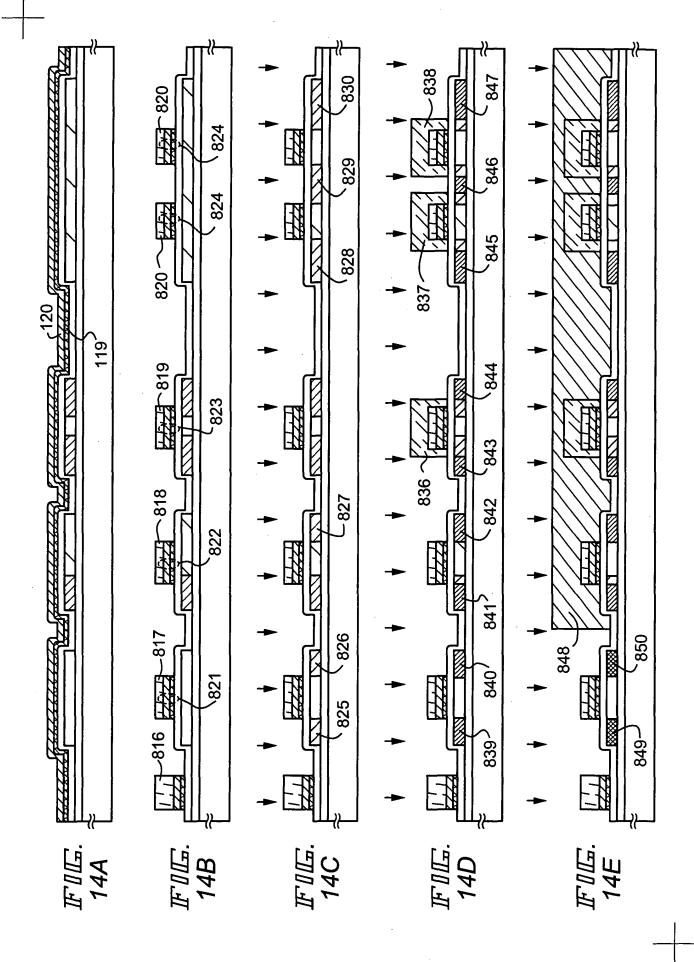


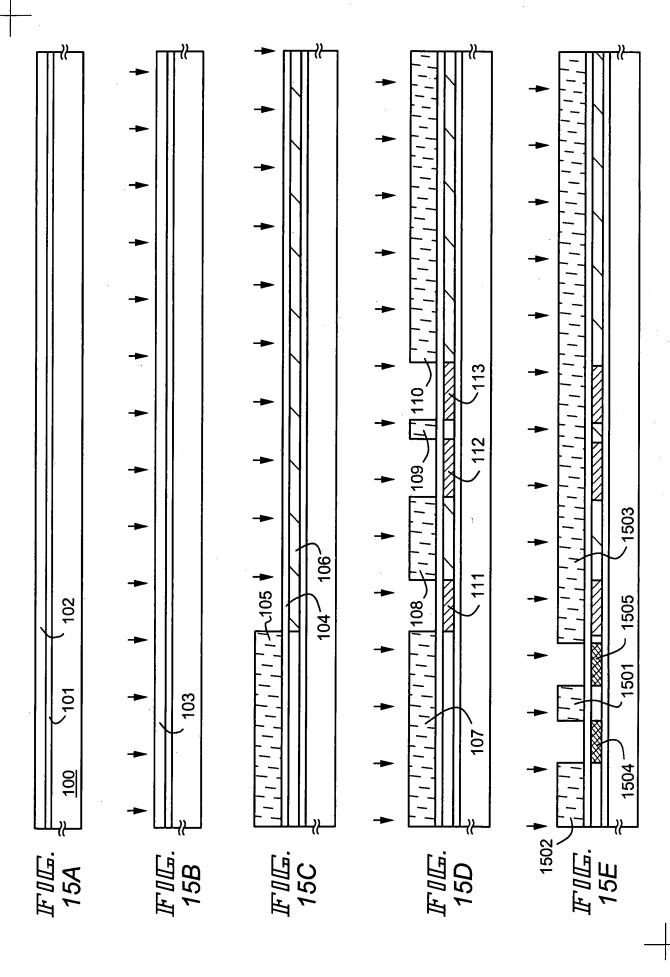


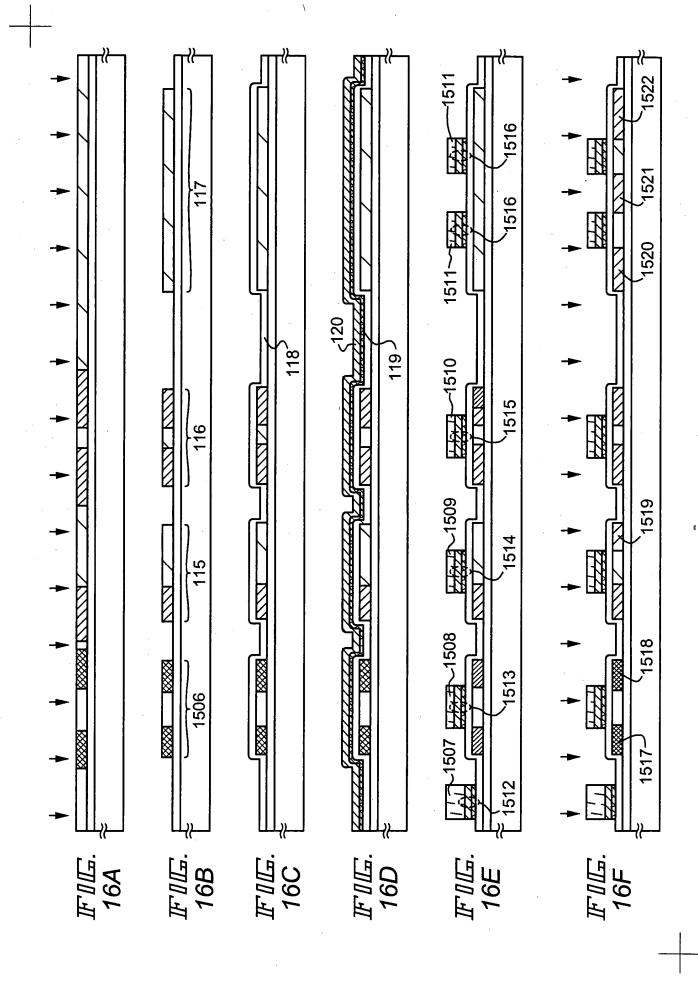


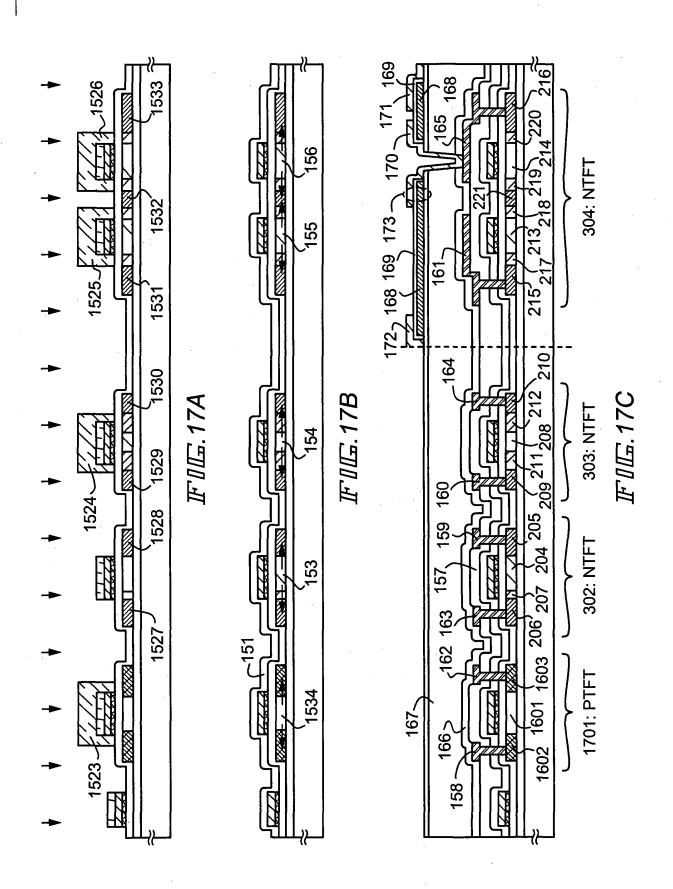


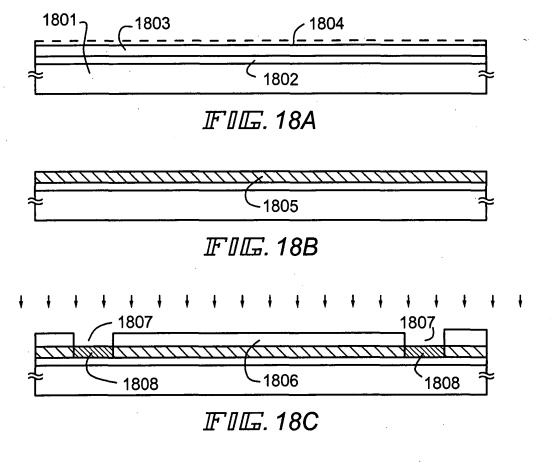






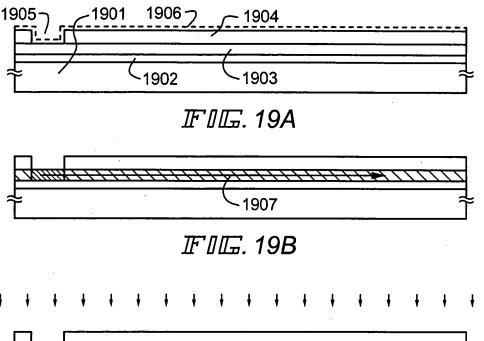


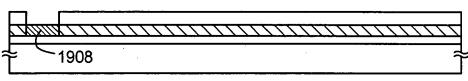




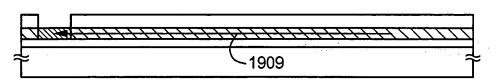
1809

FI匠. 18D

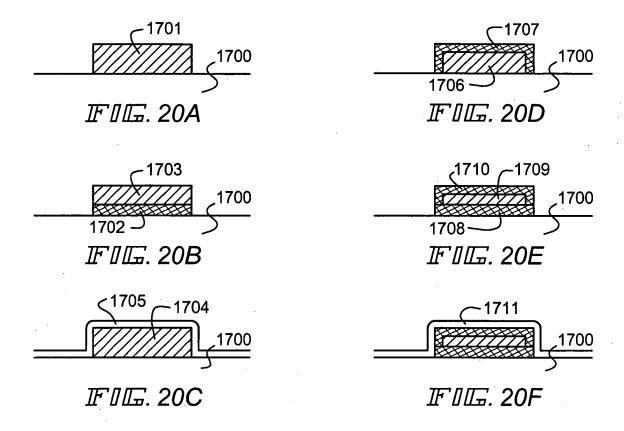


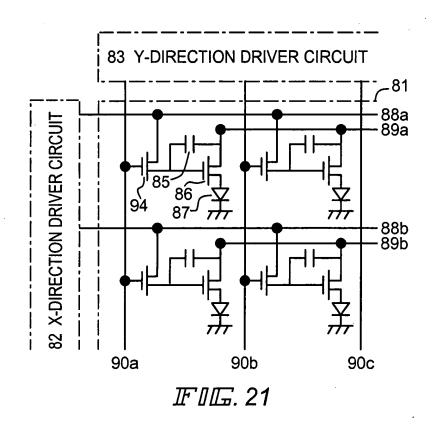


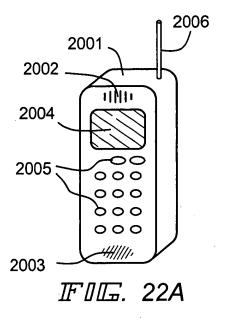
斯瓜丘. 19C

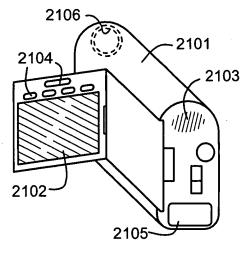


菲尼尼. 19D

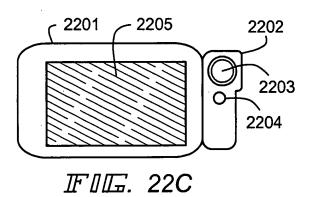


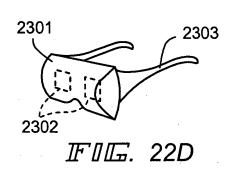


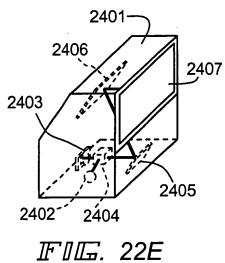


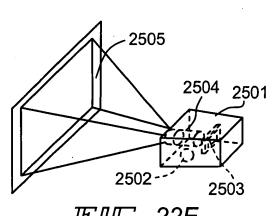


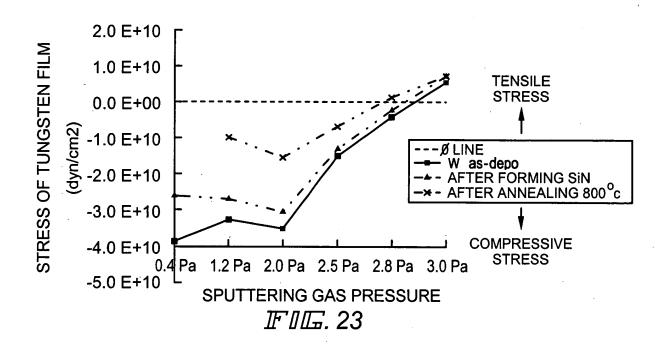
斯瓜. 22B

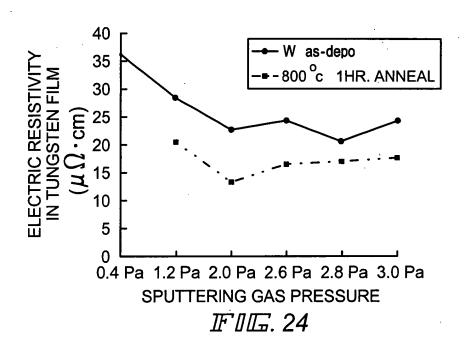


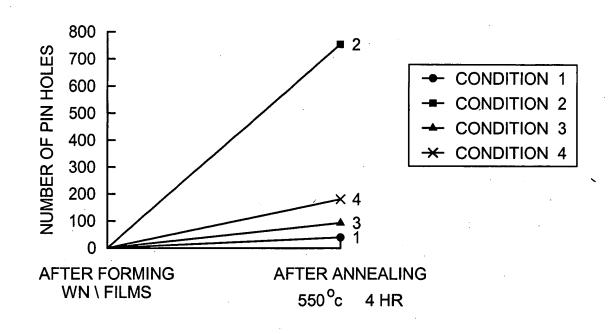




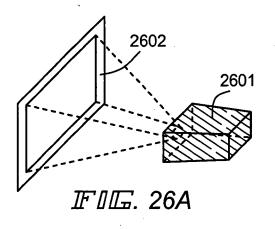


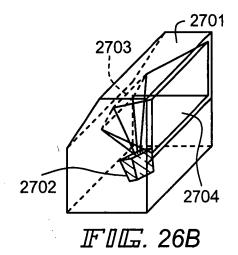






FII压. 25





2809
2809
2807
2806
2801
2801
2802

斯瓜. 26C

